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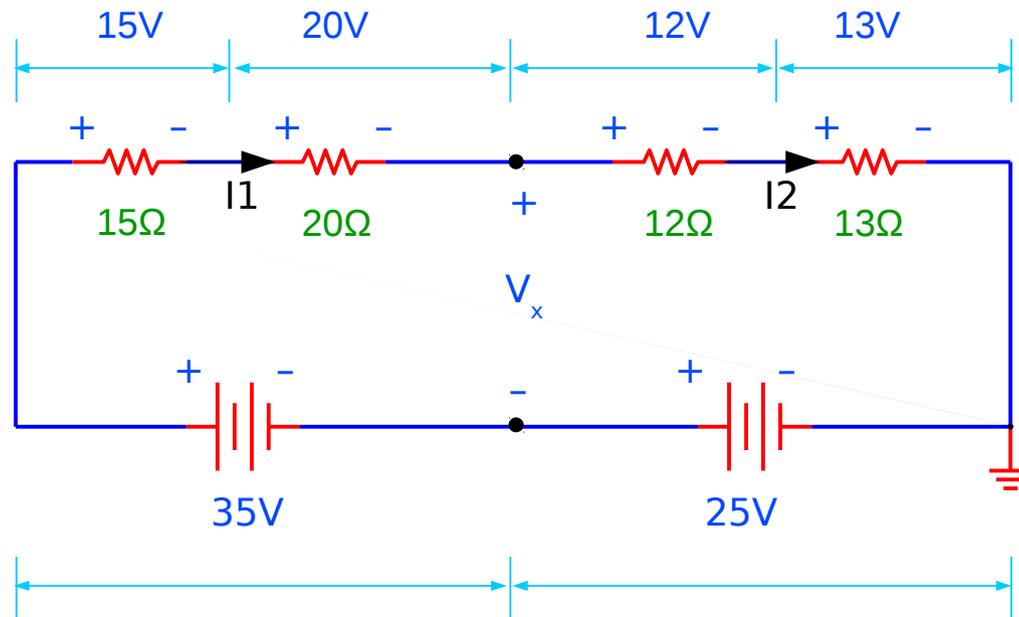
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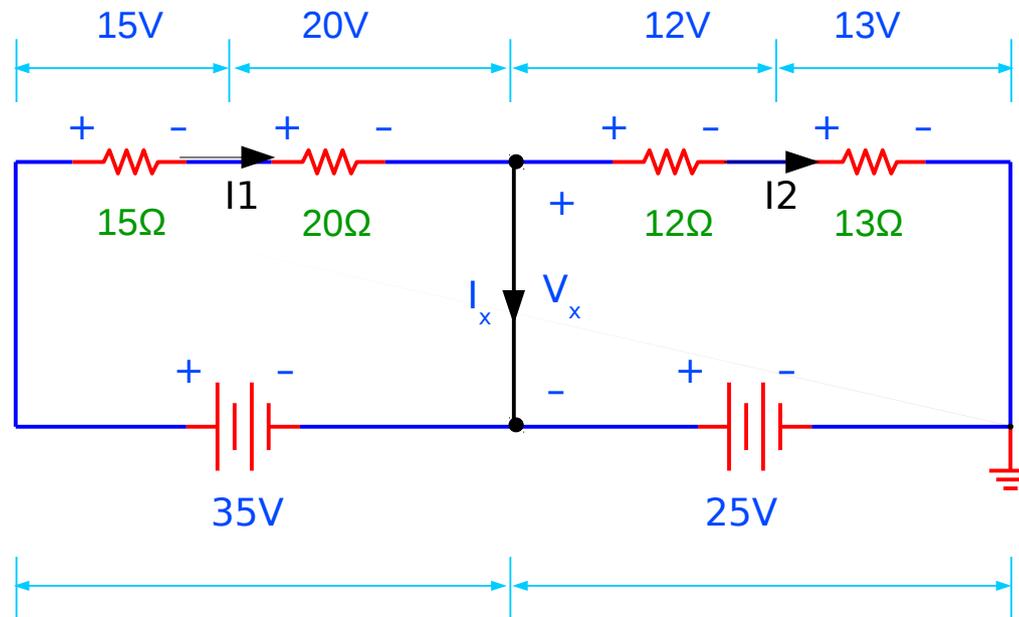
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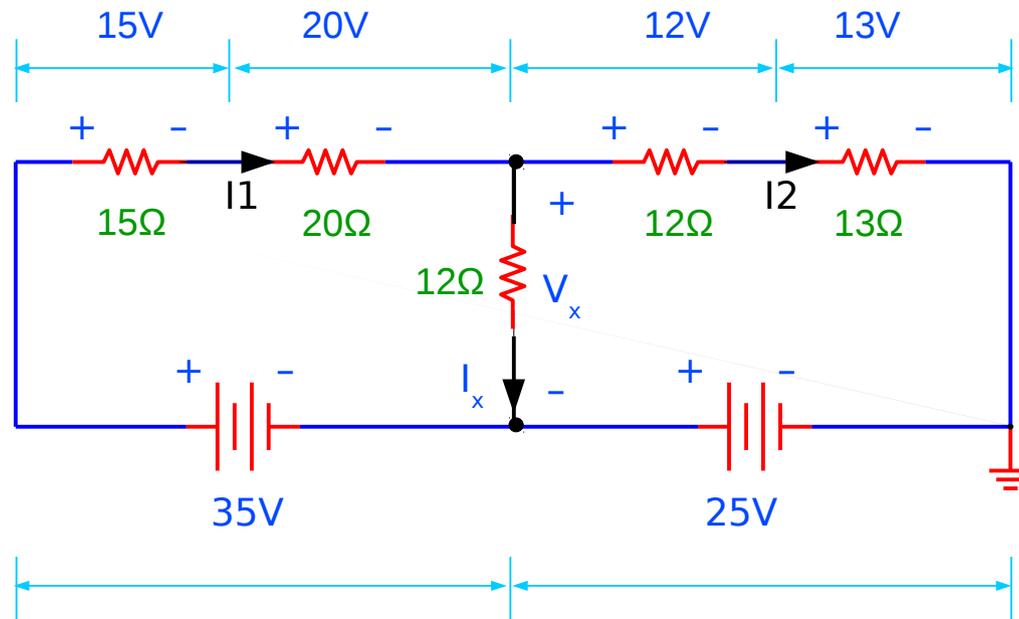
V_x , I_1 , I_2



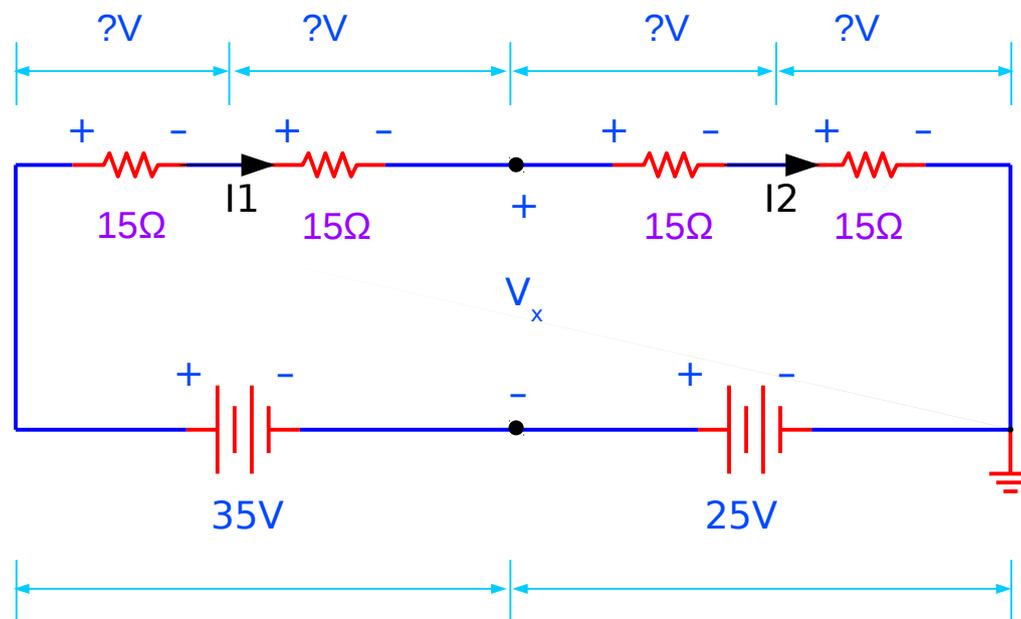
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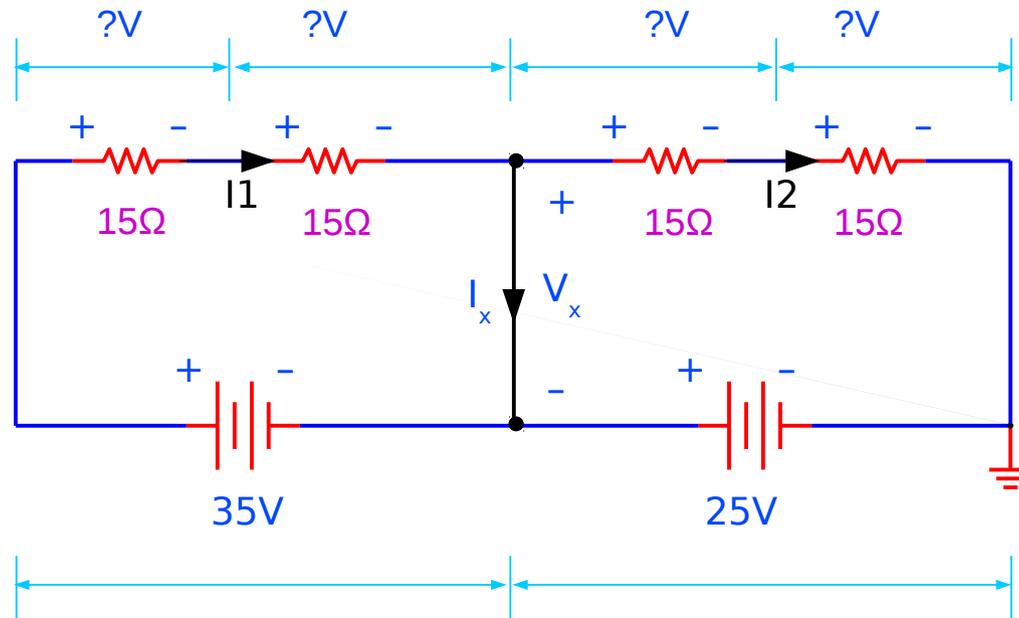
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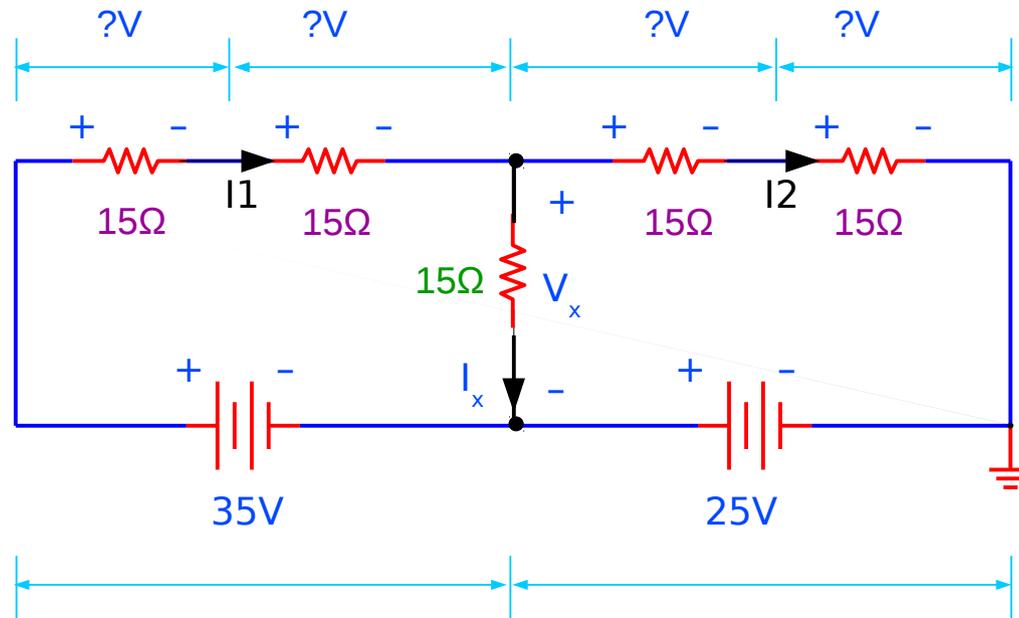
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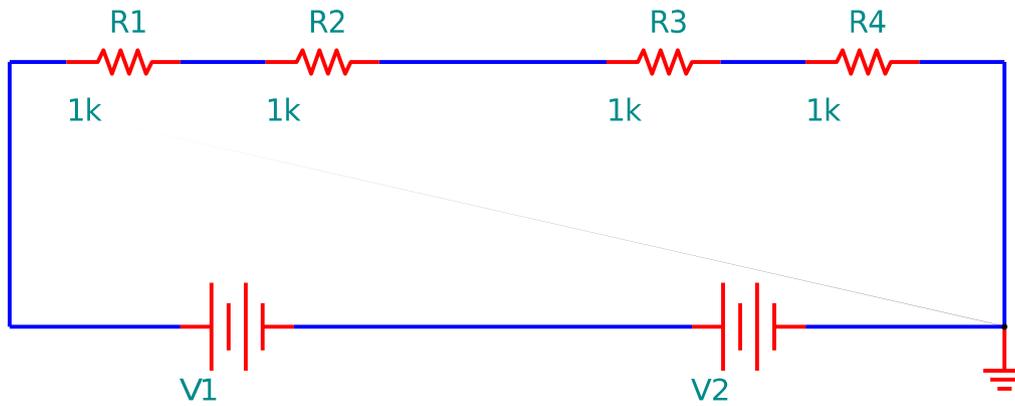


V_x , I_x , I_1 , I_2



V_x , I_x , I_1 , I_2





References

- [1] <http://en.wikipedia.org/>
- [2] www.allaboutcircuits.com
- [3]