# Idea (2A)

- Latency Minimizing Architecture
- Multi-Phase Clock
-

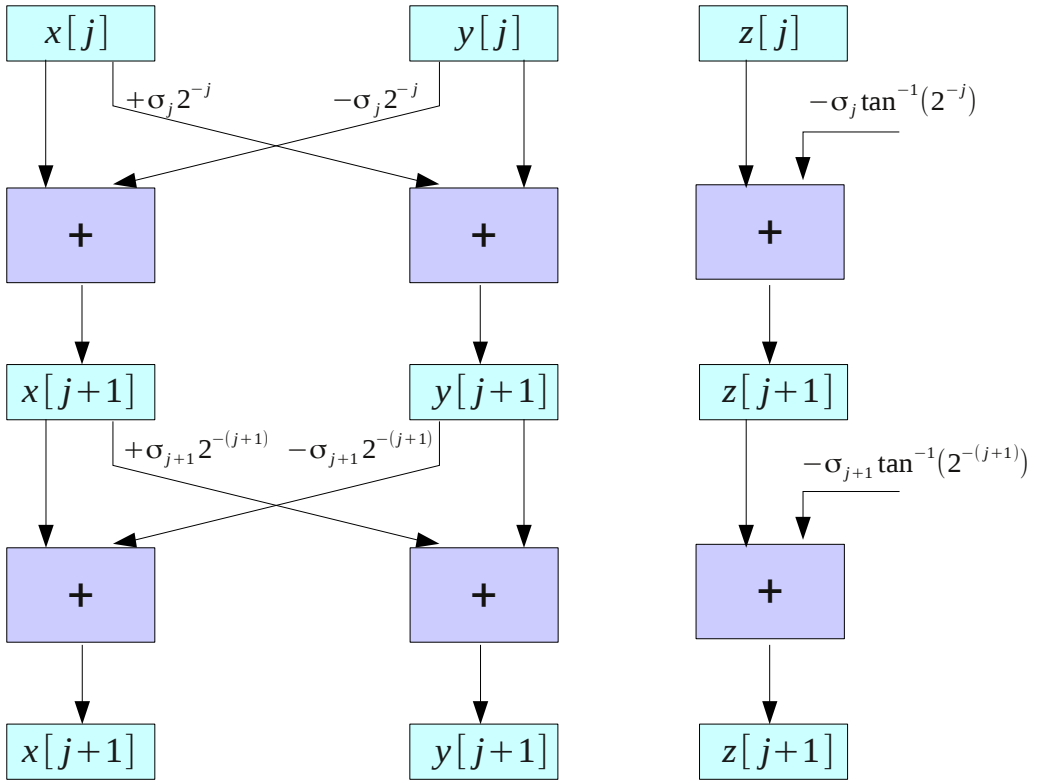
Please send corrections (or suggestions) to youngwlim@hotmail.com.

This document was produced by using OpenOffice and Octave.

# The CORDIC Equations

$$\begin{bmatrix} x[j+1] \\ y[j+1] \end{bmatrix} = \begin{bmatrix} 1 & -\sigma_j 2^{-j} \\ +\sigma_j 2^{-j} & 1 \end{bmatrix} \begin{bmatrix} x[j] \\ y[j] \end{bmatrix}$$

$$\begin{bmatrix} x[j+2] \\ y[j+2] \end{bmatrix} = \begin{bmatrix} 1 & -\sigma_{j+1} 2^{-j-1} \\ +\sigma_{j+1} 2^{-j-1} & 1 \end{bmatrix} \begin{bmatrix} x[j+1] \\ y[j+1] \end{bmatrix}$$



## References

[1] http://en.wikipedia.org/

•Branching CORDIC
•AR (Angle Recording) CORDIC
•Adaptive CORDIC
•