One Hot Design (2A)

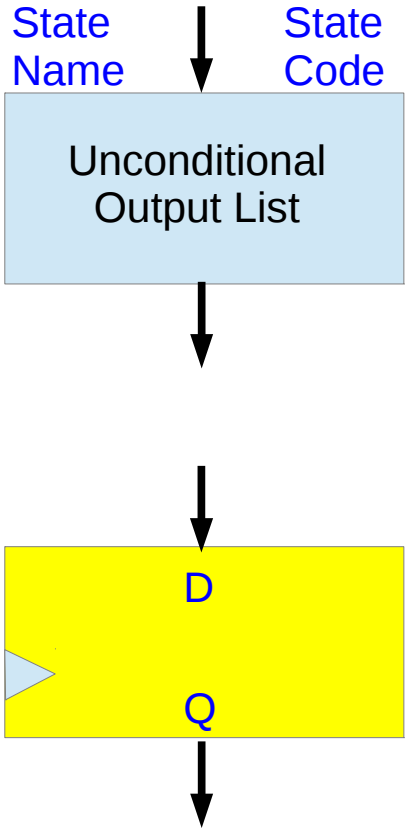
Please send corrections (or suggestions) to youngwlim@hotmail.com.

This document was produced by using LibreOffice and Octave.

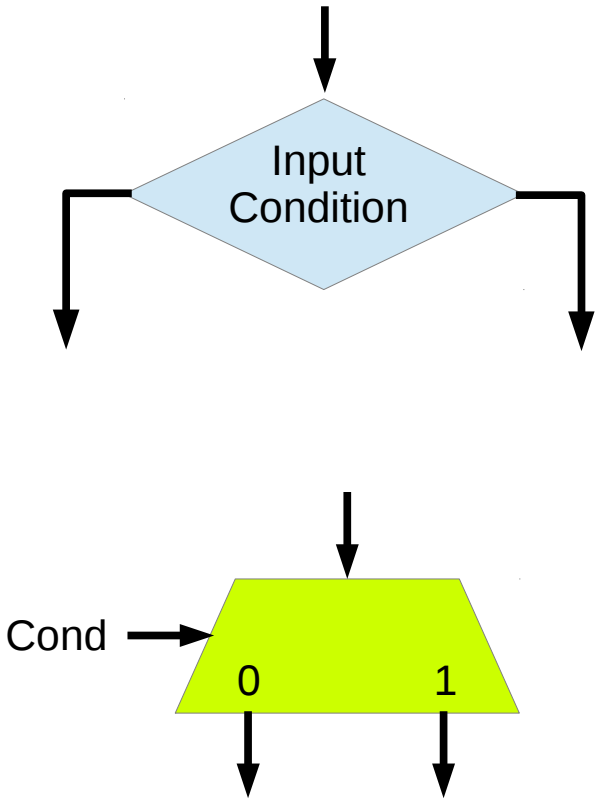
Moore ASM and One Hot Components

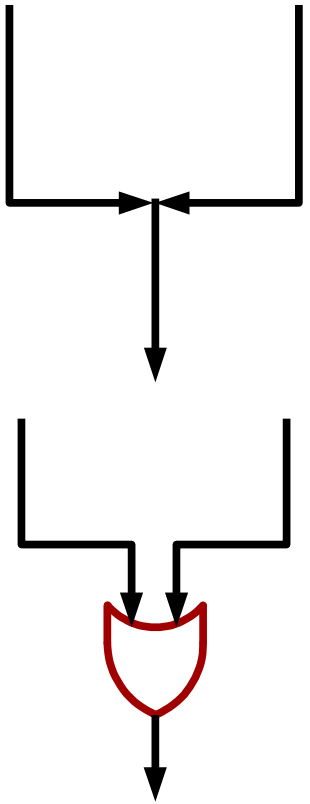
State Box

Moore Output



Condition Symbol





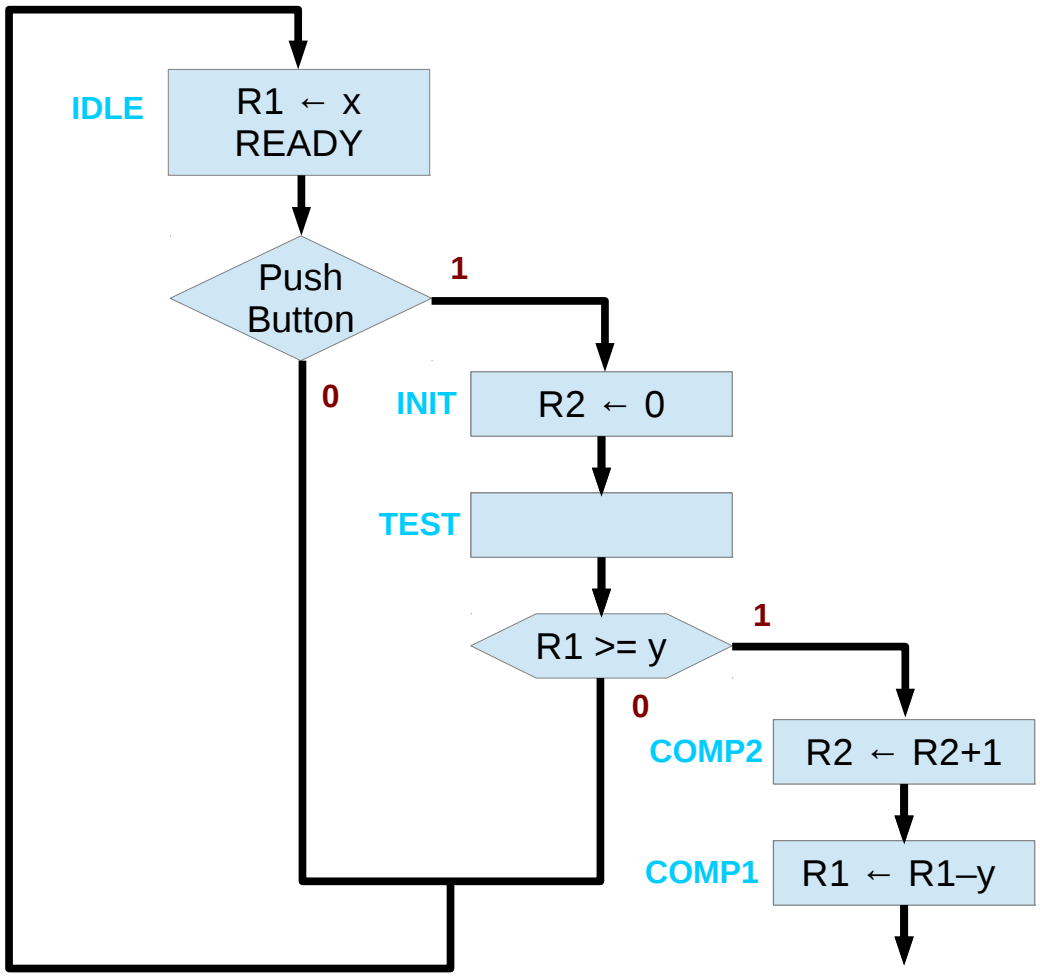
Examples

```
int x = 10, y = 3;

int R1 = x;
int R2 = 0;

while (R1 >= y) {
    R1 = R1 – y ;
    R2 = R2 + 1;
}
```

R1 = x % y
R2 = x / y



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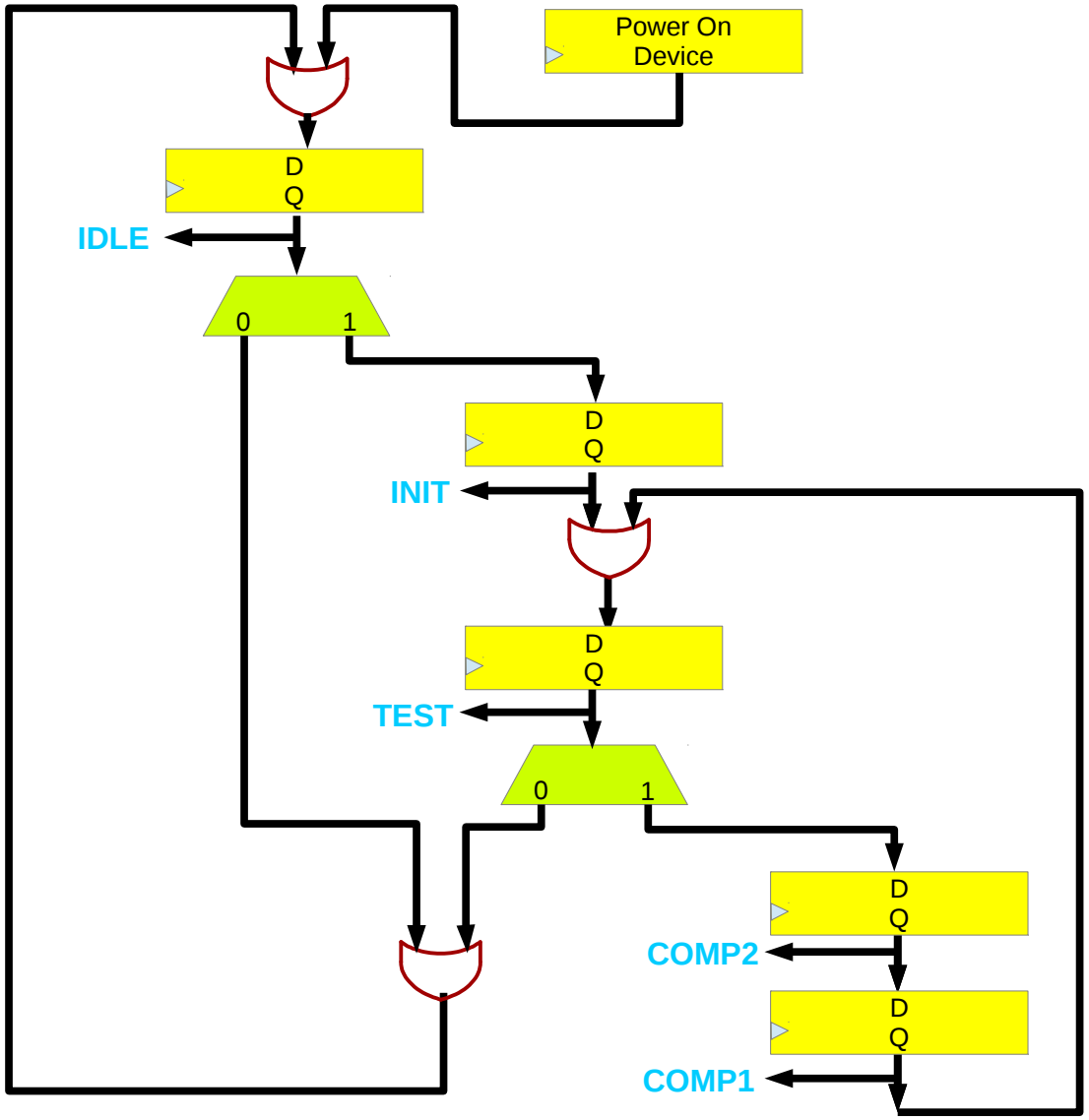

References

[1] http://en.wikipedia.org/
[2] M. M. Mano, C. R. Kime, “Logic and Computer Design Fundamentals”, 4th ed.
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[4] M. G. Arnold, “Verilog Digital Computer Design : Algorithms into Hardware”, 1999
[5] F.P. Prosser, D.E. Winkel, “The Art of Digital Design : An Intro to Top-Down Design”, 2nd ed, 1986