

Memory (1A)

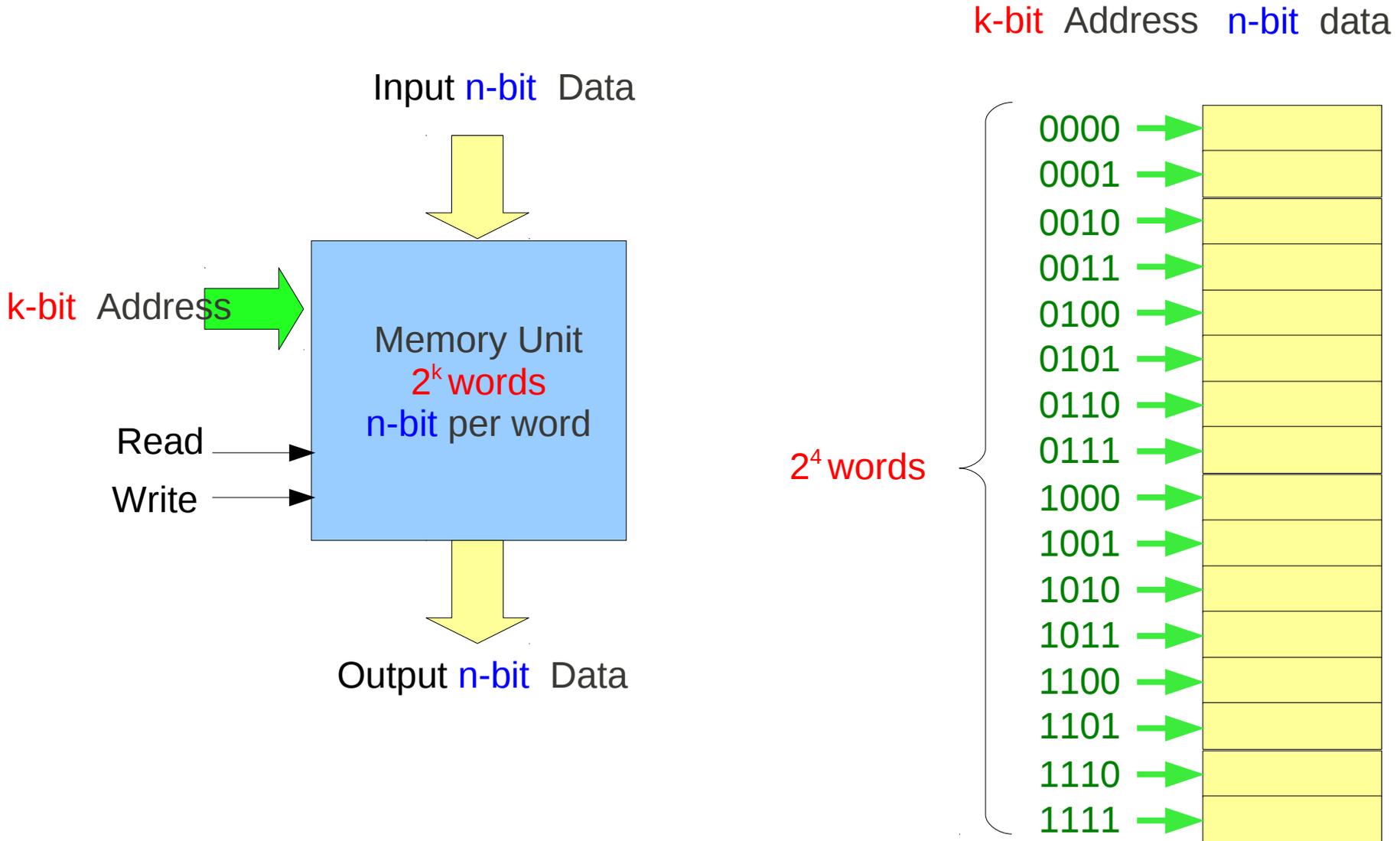
Copyright (c) 2011, 2012 Young W. Lim.

Permission is granted to copy, distribute and/or modify this document under the terms of the GNU Free Documentation License, Version 1.2 or any later version published by the Free Software Foundation; with no Invariant Sections, no Front-Cover Texts, and no Back-Cover Texts. A copy of the license is included in the section entitled "GNU Free Documentation License".

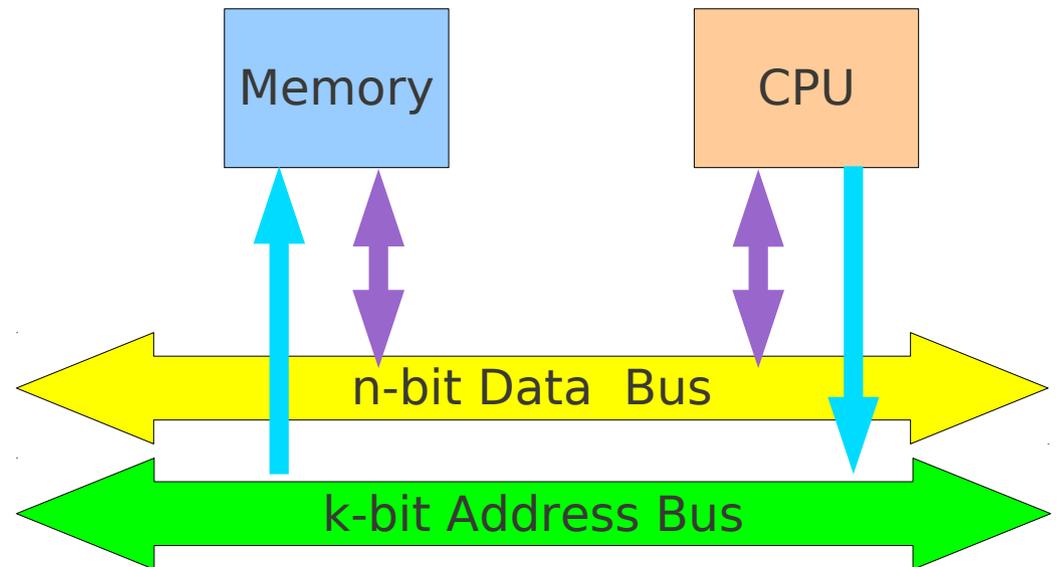
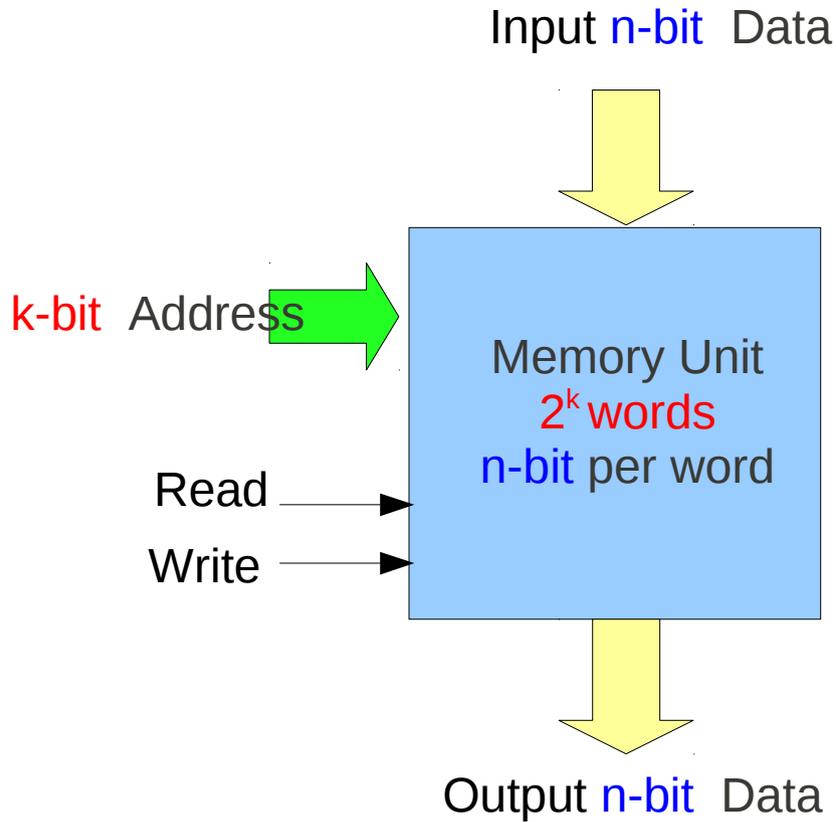
Please send corrections (or suggestions) to youngwlim@hotmail.com.

This document was produced by using OpenOffice.

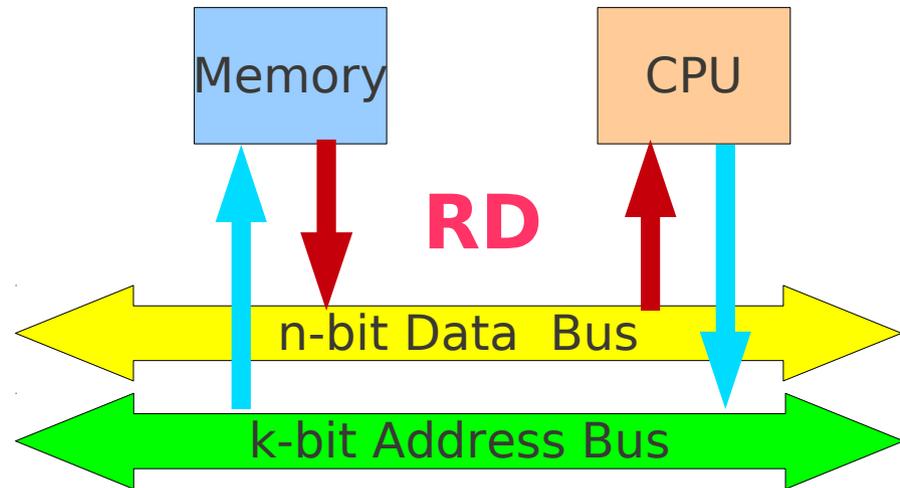
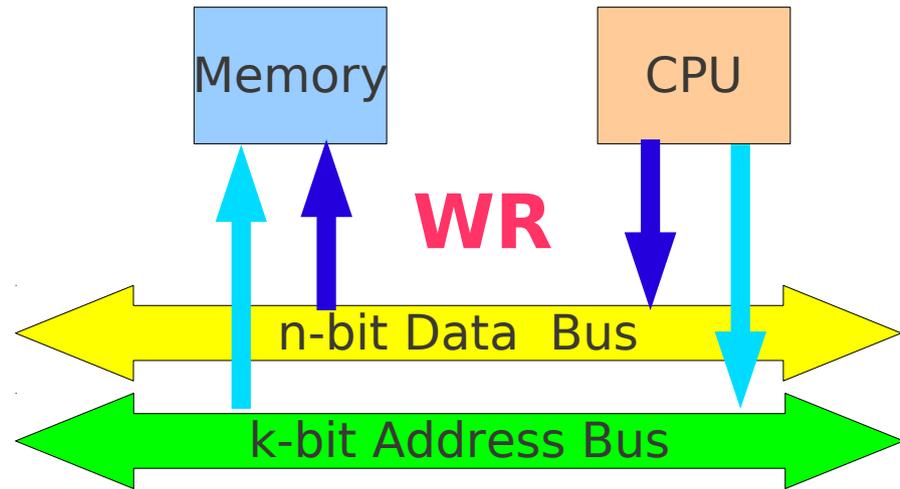
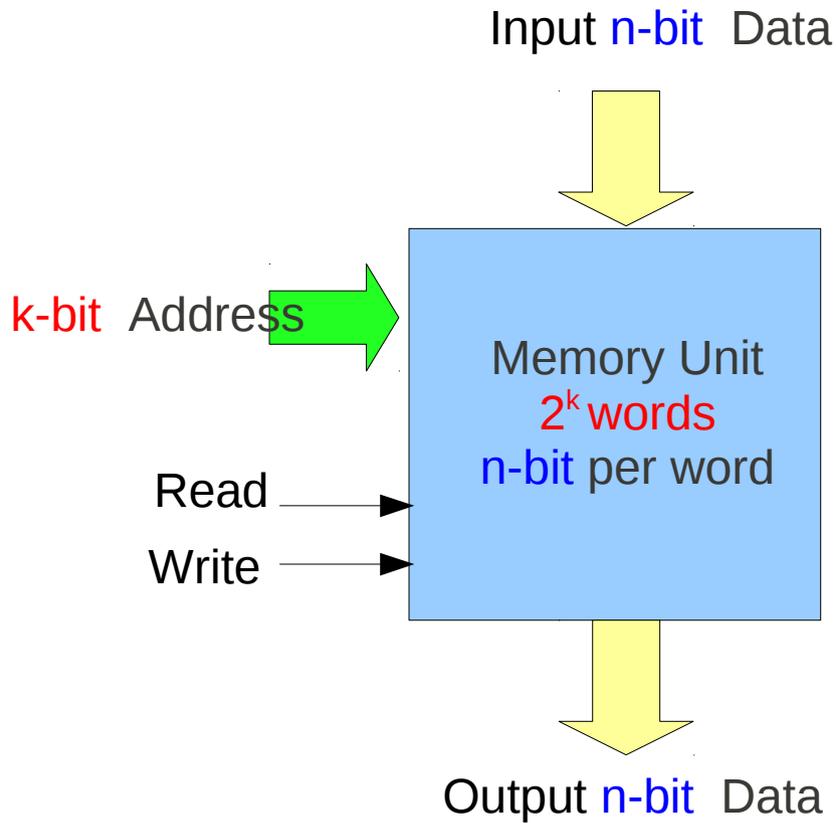
Address & Data



Address Bus & Data Bus



Memory Write

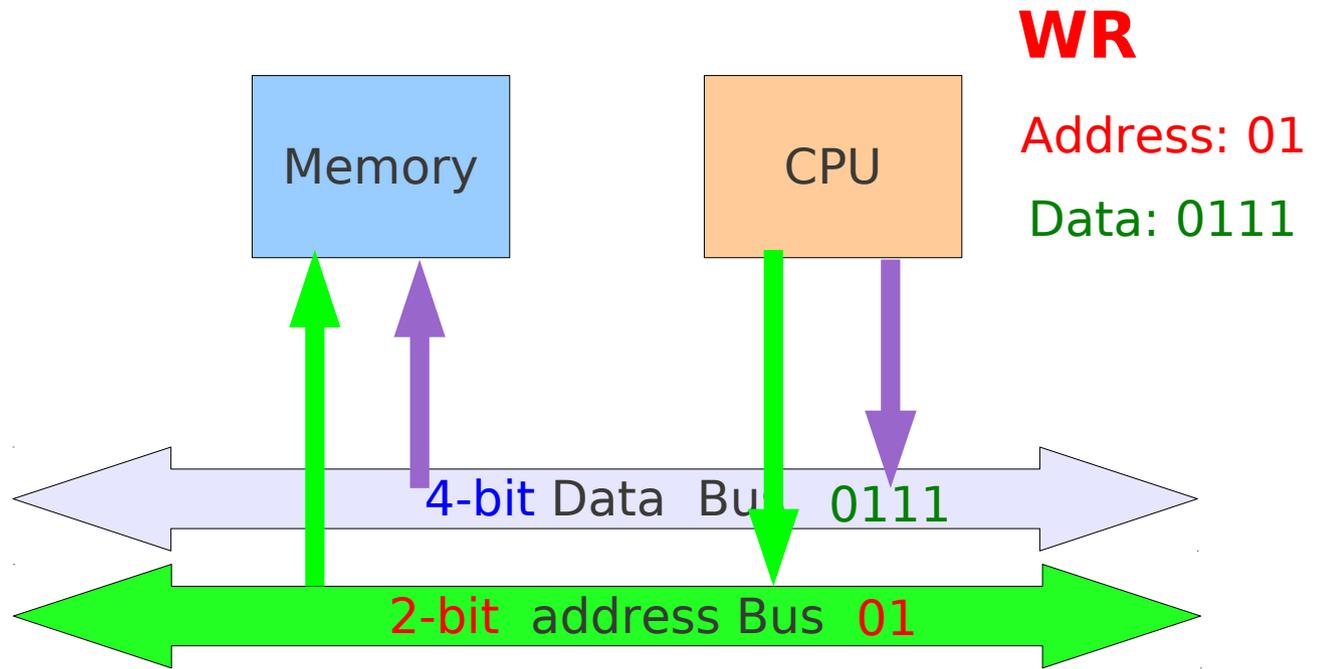


Write Example

2-bit address	4-bit data
00	0101
01	
10	1100
11	



2-bit address	4-bit data
00	0101
01	0101
10	1100
11	

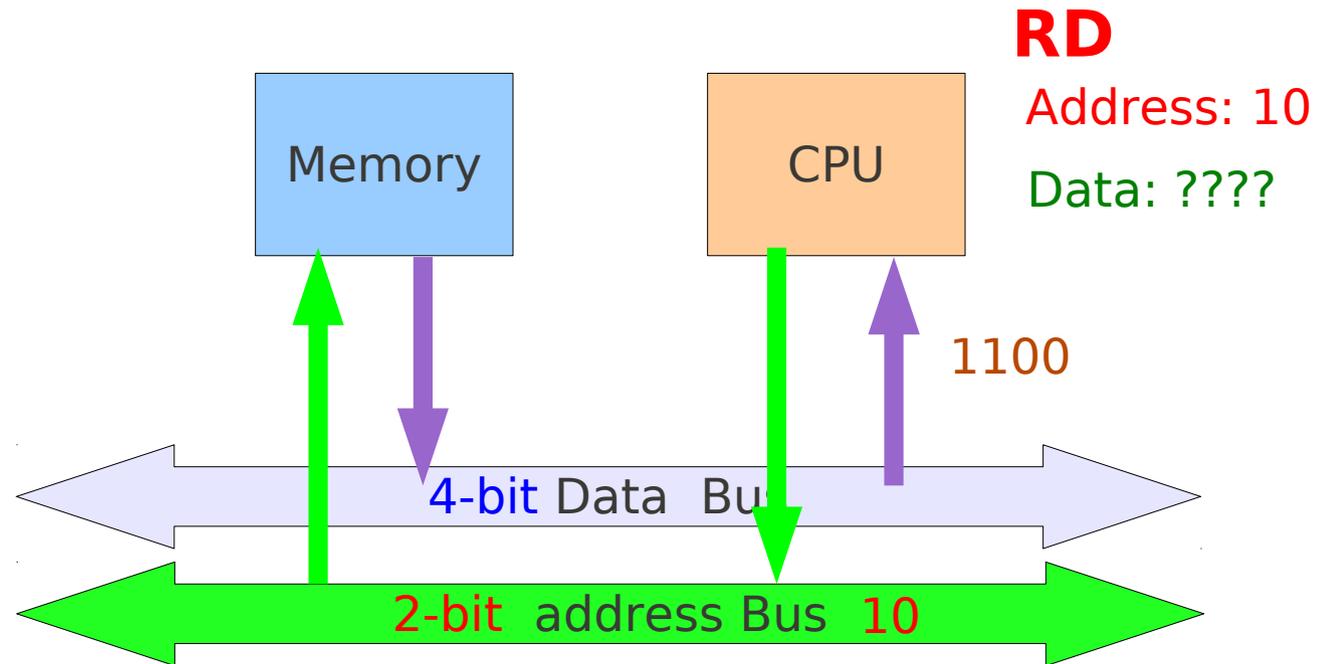


Write Example

2-bit address	4-bit data
00	0101
01	
10	1100
11	



2-bit address	4-bit data
00	0101
01	0101
10	1100
11	



Array

Array

Array

References

- [1] Essential C, Nick Parlante
- [2] Efficient C Programming, Mark A. Weiss
- [3] C A Reference Manual, Samuel P. Harbison & Guy L. Steele Jr.
- [4] C Language Express, I. K. Chun