

```
.SUFFIXES : .o .vhdl

.vhdl.o :
    ghdl -a $<

#-----
# Source Directories
#-----
MUX      = /home/young/MyWork/VLSI/a.Cblocks/Mux/rtl
PKG      = /home/young/MyWork/5.cordic_vhdl/e.package

#-----
# Target Directories
#-----
CORDIC = /home/young/MyWork/5.cordic_vhdl/b.rtl

#-----
import_files :
    \cp ${MUX}/c7 mux.vhdl      .
    \cp ${PKG}/cordic_pkg.vhdl  .

#-----
# make CONF=mux run or make CONF=rtl run
#-----
# CONF      = mux
# CONF      = rtl
#-----
# CNF_bshift = c3.bshift_tb_conf.mux.o \
# CNF_bshift = c3.bshift_tb_conf.rtl.o \
#-----
CONF      = mux

CNF_bshift = c3.bshift_tb_conf.${CONF}.o \
             \
OBJ       = cordic_pkg.o          \
           c7 mux.o            \
           c3 bshift.o          \
           c3 bshift.${CONF}.o   \
           c3 bshift_tb.o        \
           c3 bshift_tb_conf.${CONF}.o \
             \
EXE       = bshift_tb bshift_tb_conf

conf : ${OBJ} clean_exe
      ghdl -e bshift_tb_conf
```

```
run : import_files clean conf
    ghdl -r bshift_tb_conf --stop-time=1us --vcd=bshift.vcd
    # --disp-tree=inst --stop-time=1us --vcd=bshift.vcd
    # gtkwave bshift.vcd &
```

```
#-----
FILES = c3.bshift.vhdl
FILES += c3.bshift.${CONF}.vhdl
```

```
export_files :
    echo ${FILES} > filelist
    \mv filelist ${CORDIC}
    \cp ${FILES} ${CORDIC}
```

```
#-----
clean :
    \rm -f *.o *~ *# *.cf *.tar .print
    \rm -f *_tb
    \rm -f *_conf
    \rm -f *.vcd
    \rm -f ${EXE}
```

```
clean_exe :
    \rm -f ${EXE}
```

```
#-----
SRC      = cordic_pkg.vhdl
          c7 mux.vhdl
          c3 bshift.vhdl
          c3 bshift rtl.vhdl
          c3 bshift mux.vhdl
          c3 bshift tb.vhdl
          c3 bshift tb conf.rtl.vhdl
          c3 bshift tb conf mux.vhdl
```

```
print :
    more makefile ${SRC} > bshift.print
```

```
tar :
    mkdir src
    cp makefile ${SRC} src
    tar cvf bshift.tar src
    \rm -fr src
```